



NATIONAL INSTITUTE OF TECHNOLOGY SILCHAR

(An Institution of National Importance by MoE, Govt. of India)

PhD Admission under "Visvesvaraya PhD Scheme for Electronics and IT: Phase II" Sponsored by MeitY, Govt. of India, Academic Year: 2025 - 26

Ref No. NITS/VPhD/2025-26/02

Date: 03-12-2025

Digital India Corporation, Ministry of Electronics and Information Technology (MeitY), Government of India, has granted scholarships under the "Visvesvaraya PhD Scheme for Electronics and IT: Phase II". Under this scheme, admissions are currently open to the PhD degree program on a **full-time basis (one seat under the "Chip to Startup (C2S)" Project)**. This scheme has the following features.

FULL-TIME CANDIDATES:

- 1) Monthly scholarship of Rs. 38750/- for the first two years and Rs. 43750/- for the rest 3 years (support up to 5 years).
- 2) Reimbursement of Rent (ROR): As per funding agency norms, if accommodation is not provided by the Institute.
- 3) Annual contingency grant up to Rs. 1,20,000 per year.
- 4) Financial Support for attending international conferences.

Other terms and conditions: As per the Visvesvaraya PhD Scheme Phase –II, funded by MeitY.

Note: Fellowship funds will be directly deposited into the verified account of the PhD student by the Ministry, in accordance with the fellowship guidelines. For more details, candidates are required to visit <https://phd.digitalindiacorporation.in/home>

Applications are invited from Indian nationals for admission to the PhD Program for the Academic Year 2025-26, under the above scheme at NIT Silchar.

ELIGIBILITY CRITERIA:

The Minimum Qualifications for students to take admission into the PhD. Programs in Engineering Departments must satisfy one of the following criteria:

1. M.E/M.Tech. or equivalent in an appropriate area with a minimum CGPA/CPI of 6.5 (on a 10-point scale) or equivalent (60% of marks). For SC/ST candidates, a minimum CGPA/CPI of 6.0 (on a 10-point scale) or equivalent (55% of marks).
2. B.E./B.Tech. with an excellent academic record and with a CGPA/CPI of at least 8.0 (on 10 10-point scale) or equivalent (75% of marks). For graduates from IITs/NITs, the minimum CGPA/CPI requirement is 7.0 (on 10 10-point scale). For SC/ST candidates, there is a relaxation of 0.5 CGPA/CPI or 5% of marks.



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SPECIALISATIONS IN PHD PROGRAM UNDER THE SCHEME:

Sl.No	Departments	Specialization
1	Electronics and Communication Engineering "Chip to Startup (C2S)" Project	Analog VLSI, Digital VLSI, Mixed-mode VLSI, Chip design, VLSI optimisation, quantum VLSI, Neuromorphic computing, IC packaging, RTL design, Physical design, topics related to VLSI.

Under the "Visvesvaraya PhD scheme: Phase II", it is essential to work on research problems having relevance to Industry. The admission calendar, fee structure, and academic regulations will be in accordance with the regular PhD admission process of the institute. For details of the PhD regulation of NIT Silchar, please visit www.nits.ac.in.

Note: Meeting the minimum criteria does not guarantee an interview or admission. The committee may set higher shortlisting criteria.

HOW TO APPLY?

Candidates must complete the Google Form and upload a PDF file containing their updated biodata, scanned copies of the duly completed application form, relevant certificates, and mark sheets. All scanned documents must be self-attested on every page and compiled into one merged PDF file. The complete file must be uploaded using the following Google link: [Online Application Form](#)

IMPORTANT DATES:

Sl.No	Particulars	Date
1	Start of Online Application	04-12-2025
2	Last Date for submission of Online application	18-12-2025
3	Tentative Schedule For Written Test/Interview	22-12-2025

APPLICATION CHECKLIST:

Candidates must bring all original documents along with one set of photocopies of the following:

1. Duly filled-in application form (hard copy).



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2. Mark sheets/Grade cards of:
 - Secondary (10th)
 - Higher Secondary (12th)
 - All university examinations (UG/PG).
3. GATE/NET scorecard (if applicable).
4. Proof of date of birth (e.g., birth certificate or Class 10 certificate).
5. Valid community certificate (SC/ST/OBC) issued by a competent authority (not below the rank of Tahsildar), if applicable.
6. For OBC (Non-Creamy Layer) candidates, the certificate must be issued on or after 1st April 2025.
7. No Objection Certificate (NOC) from the current employer, if applicable.
8. Any other documents related to publications, patents and other testimonials.

SELECTION PROCESS

1. The admission core committee is responsible for the selection and recommendation.
2. No TA/DA will be provided.
3. Candidates should make their arrangements for the stay.
4. Shortlisted candidates will be informed by email for the written Test and Interview.
5. Final selected one candidate will be contacted through email for enrolment.

FOR FURTHER QUERIES:

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