



NATIONAL INSTITUTE OF TECHNOLOGY SILCHAR

(An Institution of National Importance by MoE, Govt. of India)

PDF Admission under "Visvesvaraya PhD Scheme for Electronics and IT: Phase II" Sponsored by MeitY, Govt. of India, Academic Year: 2026 - 27

Ref No. NITS/VPDF/2026-27/01

Date: 30-09-2026

NIT Silchar invites applications for Postdoctoral Fellowship (PDF) positions under the Visvesvaraya PhD Scheme at NIT Silchar for the academic year 2026-2027. For more details regarding the Visvesvaraya PhD Scheme, interested candidates may explore the webpage: <http://phd.digitalindiacorporation.in/>

ELIGIBILITY AND DURATION:

1. The PDF is awarded to researchers with a proven academic record, including peer-reviewed publications and recognitions.
2. It is a full-time, non-transferable, temporary position tenable in India only.
3. The fellowship is valid for **1 year**.
4. The PDF applicant should be an Indian citizen.
5. PDF applicants currently in regular employment will not be considered. However, scientists or researchers with temporary positions in academia or research institutions would be considered, but they would be required to relinquish their current roles if selected for the fellowship.
6. The PDF applicant must have obtained a PhD degree from a recognised university with first class (in terms of grades, etc.) in all preceding levels and a good academic record throughout.
7. The eligible participating institution must ensure that the PDF applicant should have completed the PhD within the past 5 years on the last date of submission of the application to the institution.
8. PDF applicants should not have completed their PhD at the same institution (host institution) where the PDF fellowship is to be awarded.
9. The upper age limit for the fellowship is 40 years on the date of submission of the application to the eligible participating institution (host institution).
10. A suitable Mentor/Guide, under whom the proposed research would be carried out, must be identified by the institution.
11. Mentor/Guide of the PDF fellow must hold a regular academic/research position in a recognised institution in India. He/she should hold a PhD degree in Science or Engineering.
12. The research proposal of the PDF applicant, to be submitted to the institution, should define clear objectives, outcomes and deliverables against the award.
13. Preference would be given to the PDF applicants who have **active industry linkages and their research proposal is directed towards industry-specific problems**.
14. Any extension beyond one year of support for PDF candidates is not admissible under the scheme.



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15. If any PDF candidate has already availed the fellowship under the scheme, he/she will not be eligible for the support.
16. Female PDF candidates may avail up to 6 months of maternity leave with fellowship support, subject to a maximum of 12 months of total fellowship support under the scheme. During maternity leave, only fellowship is admissible.

FELLOWSHIP DETAILS:

Category	Fellowship Amount	Duration	Additional Requirements
Postdoc Fellowship (PDF)	₹1,08,393/month	Up to 1 year	Contingency Grant: ₹1,00,000/year (disbursed on a pro-rata basis, e.g. ₹25,000/quarter)

SPECIALISATIONS IN PDF PROGRAM UNDER THE SCHEME:

Sl.No	Departments	Specialization
1	Computer Science Engineering	Machine Learning, Deep Learning, Artificial Intelligence, Signal and Image Processing, Natural Language Processing, Communication Systems (Wireless, Optical, Networking), Security and Cryptography, Quantum Computing, Computer Vision, Optimisation, Data Science, Bioinformatics, Health Informatics, Distributed Computing, Blockchain, and Multimedia Technologies.
2	Electrical Engineering	Control of Cyber-Physical systems, Biomedical Systems and control, Socio-Psychological Modelling, Computer Vision, Signal Processing, Intelligent Instrumentation, Semiconductor devices, Packaging, Wide Bandgap Devices, IGBT Modules, EV Charging, Solid-State Transformers, Low-Power DC-DC Converters, VLSI, Embedded Systems, Memory Design, Image Processing, IoT, Power management ICs.
3	Electronics and Communication Engineering	Signal Processing, Analog/Digital Circuits, VLSI & MEMS, Semiconductor Devices & Modeling, Micro/Nanoelectronics, Nanotechnology & Biosensors, Solar Cells, Communication Systems (Wireless, Optical, Satellite, 5G/6G, UAV, RF



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		MEMS), Antennas & Metamaterials, Information Theory, Energy Harvesting, Optimization, Machine Learning & Deep Learning, Perovskite Materials, Band Structure, RF Resonators.
4	Electronics and Instrumentation Engineering	Energy storage & management (batteries, supercapacitors, solar PV, EVs), Healthcare automation & drug delivery systems, Nanosensors & flexible electronics, 5G and beyond communication, Signal & image processing, Blockchain technology, AI/ML/DL in renewable energy and biomedical engineering, VLSI & emerging memory technologies (materials, fabrication, characterisation)

Note: Meeting the minimum criteria does not guarantee an interview or admission. The committee may set higher shortlisting criteria.

HOW TO APPLY?

Candidates must fill out the Google Form and upload a PDF file containing their updated biodata, scanned copies of the duly filled-in application form, relevant certificates, and mark sheets. All scanned documents must be self-attested on every page and compiled into one merged PDF file. The complete file must be uploaded using the following Google link: [Online Application Form](#).

IMPORTANT DATES:

Sl.No	Particulars	Date
1	Start of Online Application	01-10-2026
2	Last Date for submission of Online application	20-10-2026
3	Tentative Schedule For Interview	28-10-2026

APPLICATION CHECKLIST:

Candidates must bring all original documents along with one set of photocopies of the following:

1. Duly filled-in application form (hard copy).
2. Mark sheets/Grade cards of:
 - o Secondary (10th)
 - o Higher Secondary (12th)
 - o All university examinations (UG/PG/Ph.D.).



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3. Proof of date of birth (e.g., birth certificate or Class 10 certificate).
4. Research proposal write-up defining clear objectives, outcomes and deliverables against the award.
5. Any other documents related to publications, patents and other testimonials.

SELECTION PROCESS

1. The admission core committee is responsible for the selection and recommendation.
2. No TA/DA will be provided.
3. Candidates should make their own arrangements for the stay.
4. Shortlisted candidates will be informed by email to attend the Interview.

FOR FURTHER QUERIES:

Prof. Jyoti Prakash Mishra

The Nodal Officer.

Visvesvaraya PhD scheme, Phase II

Dept. of Electrical Engineering

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