



Department of Electronics & Communication Engineering
National Institute of Technology Silchar
Silchar - INDIA

Advertisement No: NITS/Dean (RC)/ECE/640/03/2024

Date: 09-07-2024

Applications are invited from Indian citizens for a temporary post of **Project Associate-I** in the Department of Electronics & Communication Engineering, NIT Silchar, Assam, for the following project.

Title of the project	Design and Development of Low Power Low Latency Non-Invasive Seizer Detector System.
Sponsored Agency	Ministry of electronics & Information Technology (MeitY), Government of India
Project sanction number	EE-9/2/2021-R&D-EDated 22 May 2023
No. of posts	01(one), Project Associate-I (PA-I)
Duration	1 years and extendable till the completion of the project (10/02/2027)
Principal Investigator(P.I & Co-PI)	Prof. K.L. Baishnab & Dr. Koushik Guha

General Information and Terms & Conditions

1	Essential Qualification and experiences	<ul style="list-style-type: none">• For PA-I BE/B.Tech with valid GATE Score or M.E/M.Tech in Microelectronics & VLSI Design with good academic record from a recognized University/Institute.
2	Desirable Experience for both the posts	Knowledge of working in Linux environment, Knowledge of FPGA, Knowledge of RTL Coding in Verilog, Knowledge of FPGA Implementation, Knowledge of bio-logical background related to the project work.
3	Salary	<ul style="list-style-type: none">• For PA-I, Rs.31000/- per month with valid Gate score and Rs 25000/month without valid Gate score. HRA along with other allowances/increment will be provided as per project/ institute Norms. Candidates may also get an opportunity to pursue M.Tech/PhD as per Institute Norms.
4	Last date of receiving/ Submission of filled up application form	27th July, 2024
5	Reporting	Applicants are requested to submit the signed scanned copy of their filled-in application form along with CV and the all supporting documents to the P.I. through email at klb@ece.nits.ac.in and koushik@ece.nits.ac.in with subject line“ Application for the post of Project Associate-I under MeitY project EE-9/2/2021-R&D-E”.Call letter for shortlisted candidates will be sent through email.
6	Date of interview, time, And venue	Interview date and mode (online/offline) will be notified in due course of time through email.
7	No objection certificate	Employed candidates are to submit no objection certificate from his/her Employer at the time of interview.
8	Institute right	The Institute has the discretion to restrict the number of candidates to be called for the interview on the basis of screening. No candidate maybe recruited if suitable candidates are not found.
9	Signing of the agreement	Selected candidates shall have to sign an agreement at the time of joining.

Sd/-
Associate Dean(R&C)
NIT Silchar.



National Institute of Technology, Silchar

FORMAT of APPLICATION
For Project Associate-I

Paste recent
passport
sized colored
photograph

1. Advertisement no dated:
2. Title of the Project-
3. Name of the post applied for.....
4. Department:
5. Name of the applicant (in block letters):
6. Father's/Husband's Name:
7. Date of Birth(DD-MM-YYYY):
8. Postal Address:

E-mail:

Phone no.:

9. Permanent Address:

10. Nationality:

11. Marital Status:

12. Whether belongs to SC/ST/PH/OBC:(If yes, attach certificate):

13. Educational Qualification:(Starting from Class X)

Exam Passed	Board/University	Division	%age of Marks/CGPA	Year of Passing	Subjects

14. Details of employments:

Sl. No.	Name of the Organization	Name of the post	Period	Scale of pay and present pay	Nature of duty

15. Any other relevant information that you may like to furnish:

Declaration

I declare that the above information are true and correct to the best of my knowledge and belief.

Date:

Place

Signature of the applicant