Advertisement No: NITS/Dean (RC)/ECE/640/06/2026   **Date: 22.01.2026**

Applications are invited from Indian citizens for a temporary post of **Project Associate-II (PA-II) in** the Department of Electronics & Communication Engineering, NIT Silchar, Assam, for the following project.

Title of the project	**Design and Development of Low Power Low Latency Non-Invasive Seizer Detector System.**
Sponsored Agency	Ministry of electronics & Information Technology (MeitY), Government of India
Project sanction number	EE-9/2/2021-R&D-E Dated 22 May2023
No. of posts	**One ,Project Associate-I and one Project Associate-II**
Duration	1years and extendable till the completion of the project (10/02/2027)
Principal Investigator (P.I& Co-PI)	**Prof. K.L. Baishnab &Dr. Koushik Guha**

General Information and Terms &Conditions

1	Essential Qualification and experiences	**For PA-II** M.E/M. Tech in Microelectronics & VLSI Design with minimum 7.0 CGPA with good academic record from a recognized University/Institute. **Experience** Minimum 2 Years' experience in Chip Design and Tape-out with full knowledge of VLSI CAD Tools Installation and Complete Lab Setup.
2	Desirable Experience for both the posts	Knowledge of working in Linux environment, Knowledge of FPGA Knowledge of RTL Coding in Verilog, Knowledge of FPGA Implementation, Knowledge of bio-logical background related to the project work.
3	Salary	**For PA-II** **Rs. 45,000/-** per month **+ HRA** along with other allowances/ increment will be provided as per project / institute norms Candidates may also get an opportunity to pursue PhD as per Institute Norms.
4	Last date of receiving filled up application form	**15.02.2026**
5	Reporting	Applicants are requested to submit the signed scanned copy of their filled- in application form a long with CV and the all supporting documents to the P.I. through email at **klb@ece.nits.ac.in** and **koushik@ece.nits.ac.in** with subject line" *Application for the post of **Project Associate-I/Project Associate-II** under MeitY projectEE-9/2/2021-R&D-E*".Call letter for short listed candidates will be sent through email.
6	Date of interview, time, And venue	**Interview date and mode (online/offline) will be notified in due course of time through email.**
7	No objection certificate	Employed candidates are to submit no objection certificate from his/her Employer at the time of interview.
8	Institute right	The Institute has the discretion to restrict the number of candidates to be called for the interview on the basis of screening. No candidate may be recruite difsuitable candidates are not found.
9	Signing of the agreement	Selected candidates shall have to sign an agreement at the time of joining.

Sd/-

Associate Dean(R&C)

NIT Silchar.

National Institute of Technology Silchar

FORMAT of APPLICATION

for _____

Paste recent passport size colored photograph

1. Advertisement no. ………………………….. dated:

2. Title of the Project- …………………..……………………………………………
   ……………………………………………………………………………………

3. Department:

4. Name of the applicant (in block letters):

5. Father's/Husband's Name:

6. Date of Birth (DD-MM-YYYY):

7. Age as on …………….. :

8. Postal Address:

   E-mail: Phone no.:

9. Permanent Address:

10. Nationality:

11. Marital Status:

12. Whether belongs to SC/ST/PH/OBC: (If yes, attach certificate):

13. Educational Qualification: (Starting from Class X)

Exam. passed	Board/ University	Divi sion	% age of marks/ CPI	Year of passing	Subjects

14. NET/GATE Examinations Passed:

I. NET				II. GATE		
Subject	**Qualifying date**	**Valid upto**		**Subject**	**Score**	**Valid upto**

15. Details of employments:

Sl. No.	Name of the Organization	Name of the post	Period	Scale of pay and present pay	Nature of duty

16. Any other relevant information that you may like to furnish:

Declaration

I declare that the above information are true and correct to the best of my knowledge and belief.

Date:

Place   Signature of the applicant