About NIT Silchar:

National Institute of Technology(NIT) Silchar (an Institute of National Importance under MHRD, Govt was as established in 1967 Regional **Engineering** college (REC) Silchar Assam. In 2002 upgraded to NIT. situated on the banks of Barak river with astounding green campus. The campus is fully covered by green trees making it the the beautiful institute in India. Silchar offers six UG and PG courses. The Department of ECE offers B.Tech, M.Tech and PhD degrees. The institute is well known for its notable work in technical field. The institute is having well equipped laboratories with state of art scientific Tools. It makes the Research scholar to work 24 hours to achieve new heights in technical field.

Patron: Prof. Sivaji Bandyopadhyay,
Director, NIT Silchar

Chairperson: Prof. S. Baishya, Dean Academics, NIT Silchar

Convener: Dr. Koushik Guha, Asst. Prof,

Dept. of ECE

Co-Convener: Dr. K.L. Baishnab, Asst.

Prof, Dept. of ECE

Protagonist: Prof. F.A. Talukdar, HOD Dept. of ECE

Organizing Committee

Dr. P.K. Paul, Assoc. Prof
Mrs. M.Chowdhury, Assoc. Prof
Dr. R.H Laskar, Asst. Prof
Mrs. M.Paul, Assoc. Prof
Dr. Brinda Bhowmik, Asst. Prof
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Dr. Banani Basu, Asst. Prof
Mr. Ram Kumar Karsh, Asst. Prof
Mr. Ganesh Prasad Keshri, Asst. Prof

How to reach NIT Silchar:

There are daily flights from Kolkata, Guwahati, Imphal and Agartala. Silchar is well connected by road. Buses run every day from Guwahati, Agartala and Imphal at Day and Night. The preferred bus services are Jagannath and ASTC. Meter gauze line trains are generally less-preferred in rainy season because of blockage of lines due to landslides.

Three-Days National Workshop on "CMOS-MEMS Integration": From Devices to Applications (12 to 14 April-2018)





Organized By

National MEMS Design Center @ NIT Silchar

Department of Electronics and Communication Engineering National Institute of Technology Silchar

Assam-788010, India

(An Institute of National Importance under MHRD, Govt. of India)

Convener

Dr. Koushik Guha Assistant Professor Department of ECE

About Department:

Electronics and Communication Department in NIT Silchar is most leading department in both Academic and Research streams. The Faculty and Ph.D. Scholars are fully devoted for technical research. Department is having different labs Communication lab, Antennas lab, VLSI Research Lab, Image Processing lab, Devices Lab and MEMS Lab covering almost all research areas in the field of Electronics and fully equipped with high speed PCs and state of art Equipment. MEMS and VLSI labs devoted for the miniaturization of device dimensions. The two labs are in collaboration to get new and useful devices for better society comfort. MEMS lab is having COMSOL Multiphysics, Coventor and MEMS+ licensed software's for simulation works.

Benefits of Attending Workshop:

CMOS device technology is matured in all circuit design and fabrication aspects. There are a plenty of novel topics that are presently hot topics of electronics world. MEMS Technology is relatively a new technology that people wouldn't have heard the benefits of MEMS Devices. The workshop focus on enlightening the research in the field of MEMS and CMOS state of art aspects. Participants will acquire both MEMS and CMOS advances in-depth. The workshop is intended to end up with great knowledge of advantages of MEMS-CMOS integration and its applications.

Sponsorship:

This workshop is fully sponsored by **Scientific Engineering Research Board** (**SERB**), Govt. Of India.

Three-Days Workshop on CMOS-MEMS Integration: From Devices to Applications (12-14 April-2018)

Registration Form

1.	Name
2.	Designation
3.	Organization
4.	Address
	Phone/Mobile Email:
8.	Qualification
Declaration by applicant:	
The information furnished in the application is true to best of my knowledge.	
Date:	
Signature: Forwarded By HOD	

Contact us:

Dr.Koushik Guha

Email: koushikguha2009@gmail.com,

sateeshjasti441@gmail.com

Phone: 9435072274, 9957777126.

Topics to be covered

Advancements in CMOS Technology Advances in MEMS Devices Fabrication Aspects of CMOS & MEMS Integration of CMOS and MEMS Applications of CMOS-MEMS Devices

Plenary Speakers

Dr. Roy Paily Palathinkal, IIT Guwahati

Dr. Jyotirmoy Dutta, FTD Infocom Bengaluru

Dr. K J Rangra, CSIR-CEERI Pilani

Prof. K. Srinivasa Rao, KL University

Dr. Siddharth Tallur, IIT Bombay

Prof. Prasanta Kumar Guha, IIT Kharagpur

Registration & Accommodation

Interested persons are advised to send Duly filled application on or before 06 April-2018. Afternoon Lunch and Snacks will be provided in the registration fee itself. Accommodation shall be provided in single or shared basis in the institute Guest house on chargeable basis.

Internal UG & PG Students: Rs.400

External Students and Research Scholars: Rs.600

Faculty Members & Industry Persons: Rs.1000

(DD must be drawn in favor of "Director NIT Silchar" payable at Silchar. Original DD should be submitted at the time of Registration)

Important Dates:

Last Date of accepting applications:

06th April-2018

Notification of Selection: 09th April-2018

Date of Workshop: 12th to 14th April-2018