



11<sup>th</sup> May – 11 June 2015

***Hands-on-Training  
On  
VLSI Circuit Design (Analog/Digital)  
Using Cadence Suite and Synopsys***

*Organized*

*By*

*Department Of Electronics & Communication Engineering*

*National Institute Of Technology, Silchar*

*Assam-788010*

**About The Institute:-**

National Institute Of Technology Silchar is one of the 30 National Institutes of Technology of India and was established in 1967 as a Regional Engineering college in Assam. In 2002 it was upgraded to the status of National Institute of Technology and was declared as Institute of National Importance under the national institute of technology act, 2007

**About The Department:-**

The Department of Electronics & communication offers B. Tech program in Electronics & Communication Engineering covering modern communication and software technologies. Keeping in mind the great potential and demand for new communication technologies, the Department aims at imparting its students with the latest knowledge in the field. To complement this the Department has well stocked laboratories housing various modern equipment's to enable the students to keep pace with the fast changing technology.

**Targeted participants:-**

- B.Tech/M.Tech/PhD Students
- BSc/MSc in relevant field
- Faculty/Professional Engineers

(Hostel Accommodation will be provided for students. For Faculty and others Guest House will be provided on payment basis.)

**Important Dates:-**

**Registration is open from 4<sup>th</sup> March 2015**

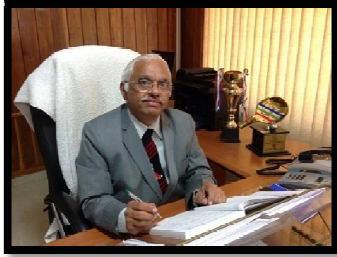
**Last date for receipt of Application form will be 30<sup>th</sup> April 2015**

**About the Training**

The training is intended to provide a vibrant opportunity for research scholars to enrich their knowledge in the area of digital IC Design. The following topics will be covered on this training.

- Review of Analog/Digital VLSI Design
- ASIC Design Flow
- VLSI Schematic circuit design (using Cadence Virtuoso/Synopsys-Custom designer)
- VLSI Layout design (using Cadence Virtuoso Layout Editor)
- Analog Environment Simulation
- Layout versus schematic simulation(LVS)
- Post Layout Simulation
- Monte Carlo Simulation
- GDS file extraction
- Introduction on SOC Encounter
- Synopsys Design vision for HDL synthesis and netlist file generation

## Patron



Prof. N. V. Deshpande  
Director NIT Silchar

## Chairman



Dr. R.H. Laskar  
HOD ECE Deptt.

## Speaker



Dr. T.R. Lenka  
Faculty ECE Deptt.  
NIT Silchar

## Speaker & Convener



Dr. K.L. Baishnab  
Faculty ECE Deptt.  
NIT Silchar

## Other speakers

- **Experts from VLSI Industries (Bangalore)**
- **VLSI Lab Experts from IIT's**
- **VLSI Lab Experts from NIT Silchar**

## **Registration:-**

Registration fees of rupees 5000.00 INR for Student & 7000.00 INR for faculty.

## **Mode of Payment:-**

❖ DD of Rs.5000.00/ Rs.7000.00, in favour of Director NIT Silchar  
Payable at SBI NIT branch. Branch Code-7061.

❖ Online payment can be made through this channel

In favour of Director NIT Silchar

A/C No.10521277057

IFSC Code-SBIN0007061

MICR Code-788002004

(Please send the scanned copy of Registration form with payment receipt)

Post the duly filled Registration Form along with the Demand Draft to  
the training Co-coordinator address mentioned below.

To,

**Dr. K.L Baishnab**

Faculty Cum VLSI Lab Incharge

ECE Department

NIT Silchar-788010

**For any query fill free to contact us in the details furnished below**

**Dr. K.L Baishnab**

**Email: [klbaishnab@gmail.com](mailto:klbaishnab@gmail.com), [klb@ece.nits.ac.in](mailto:klb@ece.nits.ac.in)**

**Contact Number-09435176506 /8011884756**

**SouravNath**

**Email: [nathsourav945@gmail.com](mailto:nathsourav945@gmail.com)**

**Contact Number-09854922053**

APPLICATION FORM FOR HANDS ON TRAINING IN VLSI DESIGN

Mandatory fields ar marked with(\*)

\*Full Name:- \_\_\_\_\_

\*Father's Name: \_\_\_\_\_

\*Address:- \_\_\_\_\_

\_\_\_\_\_

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Affix a color recent  
passport size  
photograph  
With self-attestation

College/Institute/Organization you belong:-

\_\_\_\_\_

\*Category:    student             faculty  (please check in the applicable field)

Email Id:- \_\_\_\_\_

\*Contact Number:- \_\_\_\_\_

\*DD No. \_\_\_\_\_ Date of Payment: \_\_\_\_\_

Or

\*For online payment through internet banking payment receipt should be enclosed.

Date:-

\*Signature