

NATIONAL INSTITUTE OF TECHNOLOGY SILCHAR

Silchar – 788010 (ASSAM)

No: NITS-PS-615/EE/HW & SW for Embedded Systems Lab/18(R)

Date: 14/12/2018

NOTICE INVITING TENDER

FOR SUPPLY AND INSTALLATION OF HW & SW FOR EMBEDDED SYSTEMS LAB FOR
ELECTRICAL ENGG.DEPARTMENT NIT SILCHAR



LAST DATE & TIME OF SUBMISSION

: 15/01/2019 up-to 01.00 PM

DATE & TIME OF OPENING

: 15/01/2019 at 03.30 PM



**NATIONAL INSTITUTE OF TECHNOLOGY
SILCHAR - 788 010**

Tel.No. Director: (03842) 224879

Fax: (03842) 224797

NOTICE INVITING TENDER

Adv. NITS-PS-615/EE/HW & SW for Embedded Systems Lab/18(R)

Sealed Tender/Quotations are invited from reputed Firms/Agencies/Manufacturer/Developer/Authorized Dealer/Reseller **FOR SUPPLY AND INSTALLATION OF HW & SW for Embedded Systems Lab FOR ELECTRICAL ENGG. AT NIT SILCHAR** along with Earnest Money Deposit (EMD) @2% of the total bid value in the form of Demand Draft/Bank Guarantee in favour of "The Director, NIT Silchar", Payable at Silchar. No Interest shall be paid on EMD at the time of return.

Detail specification of the item/items is given in **(Annexure – A)**.

Tender documents can be obtained from Purchase Section, NIT Silchar or may be downloaded from our website www.nits.ac.in or <http://eprocure.gov.in>. **The cost of tender document is Rs.1,000/-** (Non-refundable) to be submitted in the form of DD in favour of The Director, NIT Silchar-788010, Payable at Silchar. The last date and time for submission of Tender document will be **15/01/2019** up-to **01.00PM** and tender will be opened on the same date at **03.30 PM** in office of HOD, EE Dept., NIT SILCHAR.

The offers without Cost of Tender & Earnest Money Deposit (EMD) shall be out rightly be rejected.

Director, NIT Silchar reserves the right to extend the date or cancel the tender, accept or reject any/all quotations or not to purchase all or any of the items.

Quotations are to be sent/submitted in sealed covers addressed to:-

The Faculty-In-Charge, Purchase
National Institute of Technology, Silchar-788 010, Cachar, Assam
Email : purchasecell.nits@gmail.com

Registrar, NIT Silchar

NOTICE INVITING TENDER

Credential Criteria:

- The bidder should have provided similar nature of services to IITs/NITs/Govt. Departments/Semi Govt. Departments/PSU/Educational Institutions of National Importance etc. during last 3(three) years. **Duly certified copies are to be enclosed.**
- Quotations are to be submitted in **TWO PARTS** i.e. (a) **Technical Bid** and (b) **Price Bid**, in two separate properly sealed covers; and both these covers will have to be again put in to a single sealed cover. Also, the address of the firm submitting the quotation must appear distinctly on both the inner sealed covers, indicating also **TECHNICAL BID / PRICE BID** as may be applicable. The outer most cover shall be super scribed as
- "QUOTATION FOR SUPPLY & INSTALLATION OFFOR
..... NIT SILCHAR.
- VIDE TENDER REF NO NITS/PS-....., DATED.....
- DATE OF OPENING

[The bid will summarily be rejected & returned to the bidder if the sealed envelope containing the quotation is not super scribed as above].

- **Genuine Pricing** (Both foreign & indigenous) :Vendor is to ensure that quoted price is not more than the price offered to any other customer in India to whom this particular item has been sold recently, particularly to IIT/Institutes and other Government Organization.
- **No Part Delivery:** Part shipment for any items will not be allowed.
- **Any Optional item quoted by the supplier will not be entertained.**
- **Termination for default** : Default is said to have occurred -
- If the supplier fails to deliver any or all of the items/services within the time period(s) specified in the purchase order or any extension thereof granted by NIT Silchar, the Institute may terminate the contract / purchase order in whole or in part and forfeit the EMD/PBG as applicable.

TERMS & CONDITIONS:

1. The bidding agency should be reputed firm and having all necessary certificates, viz. GST registration certificate, PAN, Registration, Sale Tax clearance Certificate, Authorized Dealership/Distributorship certificate, etc. The photocopies of all the certificates should be attached with the tender.
2. The firm should be an original software developer/ manufacturer (OEM) in the business of developer/manufacture or supply of software for minimum 3-5 years. The firm should submit audited financial statements for latest three financial years in support of this claim.
3. The items being quoted should be of Original Manufacturer and no non-standard item should be quoted. All detailed specifications with make & model no. of the items accompanied by proper leaflets should be clearly mentioned and attached with the offer. In case of **proprietary** or patented item, necessary certificates in support of the same should be attached. The bidder must submit the Compliance Statement and Deviation Statement of technical specification.
4. The firm should have satisfactorily developed/manufactured or supplied software , as requisitioned in this tender, to IITs/NITs/Govt. Departments/Semi Govt. Departments/PSU/Educational Institutions of National Importance etc. during the last 3(three) years ending the last day of March 2018.
5. **The rate quoted must be both in words and figures and F.O.R. / Destination National Institute of Technology Silchar -788010, Assam inclusive of all charges i.e. packing, forwarding, octroi, surcharge, insurance, installation, demonstration and other charges if any. Educational discount, if any should be indicated clearly. Tenderer(s) may note that the Government of India exempts this Institute from paying custom duty/excise duty. Charges of Custom Duty (after concession as per govt. of India), IGST, Custom Clearance without any fine /damarage/ penalty shall be paid by the Institute on actual basis subject to submission of original supporting bill/ vouchers. Necessary documentation like DSIR, CDE, GST Concession Certificate, Declaration Certificate, Authorization letter regarding transportation of cleared consignment up-to NIT Silchar shall be provided by the Institute on submission of Order Acceptance and**

Proforma Invoice. Necessary documents will be furnished if required on demand by the Tenderer(s). Rate quoted for any other destination shall not be accepted and the bid will summarily be rejected. All the custom clearing issues and delivery of ordered items up-to destination i.e. NIT Silchar must be handled by the supplier only.

6. Quoted rate should be inclusive of all taxes. Nothing extra will be paid by the Institution. If there is any increase / decrease of statutory taxes will be reimbursed accordingly.
7. **Payment:** Payment 100% shall be made only after receipt of ordered items as per specification and quantity and after successful installation, demonstration, training (where applicable) and commissioning.
 - In connections to foreign items payment shall be made through wire transfer / irrevocable Letter of Credit (90% through LC and 10% after receipt of ordered items as per specification and quantity and after successful installation, demonstration, training (where applicable) and commissioning).
8. Manufacturer's/Company's name, it's trademark should be mentioned in the tender and illustrative leaflets giving technical particulars, etc. should be attached in the tender.
9. Tenderer(s) registered with the State/Central Government must quote his registration numbers, if any, and submit a xerox copy of registration along with the tender.
10. Guarantee/Warranty period offered for the tendered item is to be clearly specified.
11. The rates to be quoted by the agency should be valid for a period of **6(six) months** after the deadline date specified in the tender.
12. The quantity against each item mentioned in the tender may vary according to the actual requirements at the time of placing Purchase Order.
13. **Each bidder should clearly specify that the bidder agrees to abide by the conditions of this tender document on their printed letter head duly sealed & signed by an authorized person.**
14. **Bid Price**
 - a) The contract shall be for the full quantity as described above. Corrections, if, shall be made by crossing out, initialing dating and rewriting.
 - b) The bidder should quote the total price for each item inclusive of packing and forwarding, all duties, levies, insurance, installation, demonstration and any other charges, etc. only taxes & (discount if any) should be mentioned separately.
 - c) The rates quoted by the bidder shall be fixed for the duration of the contract and shall not be subject to adjustment on any account.
15. Each bidder shall submit only one quotation.
16. All necessary documents shall be furnished along with the bid.
17. Validity of Tenders/Quotations: Tenders/Quotations shall remain valid for a period not less than **6 (six) months** after the deadline date specified for submission of tender.
18. **Packing**
 - a) The Supplier shall provide such packing of the Goods as is required to prevent their damage or deterioration during transit to their final destination as indicated in the Contract. The packing shall be sufficient to withstand, without limitation, rough handling during transit and exposure to extreme temperatures, salt and precipitation during transit and open storage. Packing case size and weights shall have to be taken into consideration, where appropriate, the remoteness of the Goods' final destination and the absence of heavy handling facilities at all points in transit.
 - b) The packing, marking and documentation within and outside the packages shall comply strictly with such special requirements as shall be provided for in the Contract including additional requirements.
19. **Evaluation of Quotations :**

NIT Silchar will evaluate and compare the quotations determined to be substantially responsive i.e. which

 - a) are properly signed
 - b) Conform to the terms and conditions, and specifications.

20. **Award of contract:**

NIT Silchar will award the contract to the bidder whose quotation has been determined to be substantially responsive and who has offered the lowest evaluated quotation price.

- a) The bidder whose bid is accepted will be notified of the award of contract by the NIT Silchar prior to expiration of the quotation validity period. The terms of the accepted offer shall be incorporated in the purchase order.
- b) Normal commercial warranty/guarantee shall be applicable to the supplied goods.
- c) The goods (both indigenous & imported) should be insured against theft, loss or breakage during transit till destination.
- d) Upon delivery of goods, the supplier shall submit Suppliers Invoice, Insurance certificate, Warranty Certificate, or any other document as required/demand.

21. **Acknowledgement of the Purchase Order:** The supplier shall give an acknowledgement of the Purchase Order within 15(fifteen) days of the date of the Purchase Order. In case, the supplier fails to acknowledge the Purchase Order within the stipulated time, the Institute is at liberty to cancel the Purchase Order.

22. No alternations in tender forms shall be made by the bidder and if any such alteration is made, the tender is liable to be rejected.

a) Delivery Schedule and Penalty for Delay: Delivery of software should be made **within 30(thirty) days OR as per terms and condition of Purchase Order** from the date of issue of Purchase Order. **Penalty at the rate of 0.5% or part thereof of the order value per week, subject to a maximum of 2.5% will be imposed for delayed delivery and installation.**

23. **Demurrages and penalty, if any, paid by the supplier shall not be borne by the Institute.**

24. The tenders submitted shall clearly mention the name of the firm/person in whose favour the purchase order is to be placed.

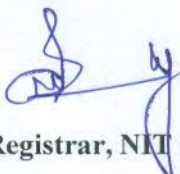
25. Contact details of the person for all post sales/installation maintenance support should clearly be given with **Name & Designation, Phone No, Fax No, Mobile, E-mail and official address.**

26. National Institute of Technology Silchar is not liable for non-receipt of the tender forms in time due to wrong address/ any delivery delay of the mail service provider/ force majeure. Tender documents received after the last date and time for receiving tenders will be summarily rejected.

27. **Successful bidder shall give a performance security @10% (as per Purchase Order) of the total order value in the form of Bank Guarantee.** The performance security shall be furnished after the order for supply is placed and before the final payment. Validity of the Performance Security shall cover the warranty period.

- The proceeds of the Performance Security shall be payable to the purchaser as compensation for any loss resulting from the suppliers failure to complete its obligations under the contract.

28. All legal disputes shall be under the jurisdiction of the Silchar Courts of Cachar District in the state of Assam.


Registrar, NIT Silchar

DECLARATION

I / We hereby declare that no case is pending with the police/ court against the proprietor/ firm/ partner or the company (Agency). Also I /We have not been suspended / blacklisted by any PSU / Government Department / Financial Institution / Court.

(Signature & seal of the contractor)

Place:

Date:

NO DEVIATION CERTIFICATE

Notwithstanding anything mentioned in our bid, we hereby accept all the terms and conditions of this tender and we do not have any deviation to this tender enquiry. We hereby undertake and confirm that we have understood the scope of work properly and shall be carried out as mentioned in this tender enquiry.

(Signature & seal of the contractor)

Place:

Date:

BIDDERS DETAILS

Name of the Contractor /Party/ Firm	:	<input type="text"/>
Name of Authorized Representative	:	<input type="text"/>
Phone Nos.	:	<input type="text"/>
Mobile Nos.	:	<input type="text"/>
Fax No.	:	<input type="text"/>
E-Mail Address	:	<input type="text"/>
Web Site Address (If Any)	:	<input type="text"/>

(Signature & seal of the contractor)

Place:

Date:

TECHNO-COMMERCIAL BID

NAME OF THE WORK: Supply and Installation of at NIT Silchar					
TENDER NO. : NITS/PS-....., Dtd....., LOS :					
Sl. No.	Name of item with Specification, Make & Model	QTY.	Rate per Unit in	Taxes in %	Total Amount in
Sub Total Amount					
Discount (if any)					
Taxes (if any)					
Other Charges (if any)					
Grand Total Amount					
(AMOUNT IN WORDS)					

I/ we have gone through all the Special & General Conditions and the contractor's obligations enclosed with this tender document and agree to abide by these.

Note: In case of discrepancy in rates between figure & words the higher will be taken for evaluation of bid and lower value will be taken for award of work.

(Signature & seal of the contractor)

Place:

Date:

CHECK-LIST (TECHNICAL BID)

SUMMARY OF COMPLIANCE TO REQUIREMENT OF TENDER

Sl. No.	Description of Requirement	Yes / No / NA	Page No.
1.	Tender Cost Rs.1000/- (Non-refundable) in the form of Demand Draft in favour of "Director, NIT Silchar" in a separate envelope		
2.	EMD @2% of total bid value in the form of Demand Draft /Bank Guarantee in favour of "Director, NIT Silchar" in a separate envelope		
3.	Copy of Manufacturer/ Authorized Supplier Certificate		
4.	Audited financial statement for the last 3 years		
5.	Copy of the PAN card.		
6.	Copy of GST registration certificate		
7.	Copies of previous work order of similar work with completion certificate (if any)		
8.	Declaration certificate		
9.	No Deviation certificate		
10.	Bidder's details		
11.	Technical Specification		
12.	NSIC/SSI Certificate where applicable		
13.	All the pages of tender document have been signed		
14.	Price bid in separate sealed envelope.		
15.	Complete copy of Techno Commercial Bid submit along with the Price Bid.		

(Signature & seal of the contractor)

Place:

Date:

ANNEXURE – A

SPECIFICATIONS FOR EMBEDDED SYSTEMS LAB

SL No	Description	Qty.
1.	<p>Software: KEIL MDK-PLUS-ED-LC; MDK-ARM Microcontroller Development Kit, Plus Edition. Node-locked license. Electronic delivery only via e-mail.</p> <p>C/C++ Compiler, Assembler, μVision IDE/Debugger/Simulator, CMSIS - RTX RTOS including source code , Middleware IPv4 Network, USB Device, File System, Graphics IPv4/IPv6 Network, USB Host, mbed components etc.</p> <p>25 user license</p>	1
2	<p><u>ARM7 Evaluation Board:</u></p> <ul style="list-style-type: none"> • It should be possible to expand this board with additional hardwares like Logic controller Interface, LCD (16X2) Interface, Stepper Motor Interface with Stepper Motor (3kgcm) & Power Adapter, Traffic Lights Interface, Seven segment Display, Elevator Interface, TXDR Interface using PT100 with ADC, Crystal Clock Divider Interface, Musical Tone Generator Interface, 8 Channel 12 Bit ADC Interface with MUX etc by using a piggy-back interface adapter with 26 pin header and 5V power supply should be provided for interfacing ARM7 board to expandable interfacing modules • Having LPC2148 ARM7TDMI-S based high-performance 32-bit RISC Microcontroller • On-Chip Memory: 512KB Flash & 32KB RAM • On-chip flash programming support through LPC2000 Flash Utility or Flash Magic Utility • Two nos. of on-chip UART ports COM0 and COM1 are terminated on two 9 pin D type (female connectors) • It includes speaker controlled by the output of the on-chip D/A converter • Two 10bit ADCs with 14 channels • USB 2.0 Full Speed Device Controller • Two I2C serial interfaces • Two SPI serial interfaces • Two 32-bit timers • Watchdog Timer, PWM unit, Real Time Clock with optional battery backup, Brown out detect circuit General purpose I/O pins. CPU clock up to 60 MHz, On-chip crystal oscillator and On-chip PLL. • Boards should be supplied with schematic diagrams, necessary cables, power supplies etc. • Target board should be compatible with industry leading IDE/Software like KEIL 	5
3.	<p><u>ARM Cortex-M3 Evaluation Board:</u></p> <ul style="list-style-type: none"> • It should be possible to expand this board with additional hardwares like Logic controller Interface, LCD (16X2) Interface, Stepper Motor Interface with Stepper Motor (3kgcm) & Power Adapter, Traffic Lights Interface, Seven segment Display, Elevator Interface, TXDR Interface using PT100 with ADC, Crystal Clock Divider Interface, Musical Tone Generator Interface, 8 Channel 12 Bit ADC Interface with MUX etc by using a piggy-back interface adapter with 26 pin header and 5V power supply should be provided for interfacing ARM Cortex-M3 board to expandable interfacing modules • 100MHz LPC1768 ARM Cortex™-M3 processor-based MCU in 100-pin LQFP • On-Chip Memory: 512KB Flash & 64KB RAM • Color QVGA TFT LCD • 10/100 Ethernet Port 	5

	<ul style="list-style-type: none"> • USB 2.0 Full Speed - USB, USB-OTG, & USB Host • 2 CAN Interfaces • 2 Serial Ports • SD/MMC Card Interface • 5-position Joystick and push-button • Analog Voltage Control for ADC Input • Amplifier and Speaker • 70 GPIO pins • Debug Interface Connectors <ul style="list-style-type: none"> ◦ 20 pin JTAG (0.1 inch connector) ◦ 10 pin Cortex debug (0.05 inch connector) ◦ 20-pin Cortex debug + ETM Trace (0.05 inch connector) • Boards should be supplied with schematic diagrams, necessary cables, power supplies etc. • Target board should be compatible with industry leading IDE/Software like KEIL 	
4.	<p><u>ARM Cortex-M4 Evaluation Board:</u></p> <ul style="list-style-type: none"> • 168MHz STM32F407IG ARM Cortex™-M4 processor-based MCU in 176-pin BGA • On-Chip Memory: 1MB Flash & 192KB RAM • External Memory: 8MB NOR Flash, 512MB NAND Flash, 2MB SRAM, 8KB I2C EEPROM with NFC interface • 2.4 inch Color QVGA TFT LCD with resistive touchscreen • 10/100 Ethernet Port • USB 2.0 Full Speed - USB, USB-OTG, & USB Host • USB 2.0 High Speed - USB, USB-OTG, & USB Host • 1 CAN Interface • Serial/UART Port • MicroSD Card Interface • 5-position Joystick • 3-axis digital Accelerometer • 3-axis digital Gyroscope • Analog Voltage Control for ADC Input (potentiometer) • Audio CODEC with Line-In/Out and Speaker/Microphone • Digital Microphone • Digital VGA Camera • Push-Buttons for Reset, Wakeup, Tamper and User • 8 LEDs directly connected to port pins • Power Supply via: <ul style="list-style-type: none"> - High Speed and Full Speed USB (micro) connectors - Power jack (8V-12V) with Voltage Regulator capable to supply both USB host interfaces (500mA each) • Debug Interface Connectors: <ul style="list-style-type: none"> - 20 pin JTAG (0.1 inch connector) - 10 pin Cortex debug (0.05 inch connector) - 20-pin Cortex debug + ETM Trace (0.05 inch connector) 	5
5.	<p><u>Debugger :</u></p> <ul style="list-style-type: none"> • Debugger should support all popular IDE's. • Debugger should support unlimited breakpoints in flash memory. • Debugger should also include support for GDB server. • Debugger should support JTAG speed up to 12MHz. • Direct download into flash memory of most popular microcontrollers. • Supported CPUs: Any ARM7/9/11, Cortex-A5/A8/A9, Cortex- 	5

	<p>M0/M1/M3/M4, Cortex-R4, RX610, RX621, RX62N, RX62T, RX630, RX631, RX63N</p> <ul style="list-style-type: none"> • Full-speed USB 2.0 interface • Serial Wire Debug supported • Serial Wire Viewer supported • Download speed up to 1 MByte/second • RDI interface available, which allows using debugger with RDI compliant software. • Free software updates • Cross platform support (runs on Windows, Linux, Mac OS X) 	
6.	EXPANDABLE INTERFACE MODULES:	
	8 Channel 12 Bit ADC Interface without MUX	5
	8 Channel 12 Bit ADC Interface with MUX	5
	16 Channel 8-bit ADC Interface	5
	DAC for ADC & Temp. Sensor Interface	5
	Dual DAC Interface	5
	Dual Slope ADC Interface	5
	4-Digit, 7-Segment LED Display Interface	5
	Elevator Interface	5
	Calculator type keyboard interface	5
	Logic Controller Interface	5
	LCD (16x2) Interface	5
	Musical Tone Generator Interface	5
	Opto Isolated Input Interface	5
	Opto Isolated Output Interface	5
	Relay Output Interface	5
	Real Time Clock Interface	5
	Stepper Motor Interface with Stepper Motor (3Kgcm) & Power Adapter	5
	Traffic Lights Interface	5
	Crystal Clock Divider Interface	5
	DC Motor Interface	5
	TXDR Interface using PT100 with ADC	5
	4x4 Matrix Hex Keypad Interface	5
	6-digit, 7-Segment Display with Calculator KBD Interface	5