

OFFICE OF THE HEAD
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY SILCHAR

No. EIE/ /19

Date: 29.01.2019

Shortlisted candidates for PhD Admission

The following candidates have been shortlisted (provisionally) for written test and interview before the selection committee for admission into PhD program in the Department of E&I Engineering, NIT Silchar for the session January- June, 2019.

Sl. No.	Name	E-mail ID	Category of Admission
1	Natha Dhnunjayarao	dhanunjayaraonatha@gmail.com	Group-A
2	Jusmita Das	jusmita.nita2008@gmail.com	Group-A
3	Sushanta Reang	susantameska2307@hmail.com	Group-A
4	Anurupa Ghosh	anurupa.etce@gmail.com	Group not mentioned
5	Nayan Jyoti Boro	nayanjyotiboro30@gmail.com	Group-A
6	Sohel Das	dassohel43@gmail.com	Group-A
7	Mithilesh Kumar	mithilesh02kumar93@gmail.com	Group-A
8	Ashish Kumar Sarangi	ashishsarangi1944@gmail.com	Group-A
9	Takhellambam Gautam Meitei	gtm820pm@gmail.com	Group-A
10	Anushmita Pathak	anushmitapathak@gmail.com	Group-A
11	Tabassum Rasul*	tabassum0205@gmail.com	Group-A
12	Santosh Kumar Boddupelli	santoshb4@gmail.com	Group-A
13	Sanjit Brahma	sanjitbrahma5@gmail.com	Group-A
14	Prabin Kumar Bera	prabinbera@gmail.com	Group-A
15	Soumik Chakraborty	soumik890@gmail.com	Group-A
16	R. Shivaji**	shivaji147@gmail.com	Group-A

Sl. No.	Name	e-mail ID	Category of Admission
1	Ranjeet Kumar Pathak	pathak84@yahoo.co.in	Group-B
2	Shaik Mohammed Ali	alimohammed4@gmail.com	Group-B

* Provisionally shortlisted for written test and interview. The candidate has to submit their original DD on the day of reporting.

** Provisionally shortlisted for written test and interview. A fair hard copy of application form & DD along with copy of required documents are to be submitted on the day of reporting.

Date & time of written test & interview:

Date of reporting: 14th February, 2019

Time: 9:00 a.m. to 10 a.m.

Venue: Office of the HOD, Department of E&I Engineering.

N.B.

- i. Employed persons must produce a sponsorship certificate/NOC, if a copy of the same has not been attached earlier along with the application.
- ii. Candidates have to produce all original mark sheets, certificates/ testimonials/ valid GATE score card for verification at the time of interview.
- iii. No TA/DA shall be admissible to the candidates for appearing for the interview.

Note: For any queries please contact HOD, Electronics & Instrumentation Engineering, NIT Silchar, Assam.

Sd/-

Head,
Dept. of Electronics & Instrumentation Engineering,
NIT Silchar