

**About the Institute:**

National Institute of Technology, Silchar is one of the 30 National Institutes of Technology of India and was established in 1967 as a Regional Engineering College in Assam. In 2002 it was upgraded to the status of National Institute of Technology and was declared as Institute of National Importance under the National Institutes of Technology Act, 2007.

The climate of Silchar during January is little cold with temperature around 12-18°C in the day time.. NIT Silchar is a fully residential institution with nine hostels for boys and two hostels for girls. It has six engineering degree offering branches and five non-engineering branches.

**About the Departments:**

The Department of Electronics and Communication Engineering is established in 1983. The department has been striving to provide quality technical education since its inception. At present, the department is offering B. Tech. in Electronics and Communication Engineering and M. Tech. with specialization in Microelectronics and VLSI Design and Communication and Signal Processing, respectively. The department is also offering Ph.D. programme in various areas like VLSI, MEMS, Signal Processing, Antenna Design, Microwave Engineering, Communication Engineering, etc.

**Eligibility:**

This program is open to industrial personnel, faculty members and students including research scholars, PG and UG students of recognized technical institutions.

**Registration Procedure:**

Interested candidates are advised to fill the registration form and email scanned copy of the followings to course coordinator, at [ujjaldgp@gmail.com](mailto:ujjaldgp@gmail.com),

(i) Filled registration form

(ii) Identity card

(iii) Payment proof

Participants will receive an acknowledgement on acceptance of their application over mail.

All participants have to pay the registration fees before submitting application through

Online transfer to the account of the

Director, NIT Silchar, Account No.: 10521277057

IFSC: SBIN0007061 Branch: State Bank of India, NIT Silchar. Brochure and registration form

can also be downloaded from:

<http://www.nits.ac.in/>

**Registration Fee**

\*Internal Students: Rs. 500/-

\*External Students: Rs. 1000/-

\*Internal Faculty members: Rs. 1000/-

\*External Faculty members: Rs. 3000/-

\*Industry Persons: Rs. 5000/-

**Accommodation, Travel and General Information**

All the out station participants will be provided accommodation on payment basis in the Guest House/Hostels of the Institute. No TA and DA will be paid to the participants. Participants will be provided high tea, lunch etc. during the day session from January 21 – January 25, 2019. Participants will also get the registration kit & course material.

**How To Reach NIT Silchar**

There are daily flights from Kolkata, Guwahati. Taxi are available from Airport to NIT Silchar, Silchar is also well connected by road and train.

**Address for Communication:**

Dr. Ujjal Chakraborty, Coordinator  
Assistant Professor, Department of ECE  
Mobile: +91-9563606800  
Email: [ujjaldgp@gmail.com](mailto:ujjaldgp@gmail.com)

## Workshop On Emerging Devices, Circuits and Systems (EDCS 2019)

(Under TEQIP-III)

21<sup>st</sup> to 25<sup>th</sup> January 2019

Coordinator

**Dr. Ujjal Chakraborty**  
Assistant Professor, Dept. of ECE



**Organized by the Department of  
Electronics & Communication Engineering**

(An Institute of National Importance)  
Silchar, Cachar, Assam, India, PIN-788010

[www.nits.ac.in](http://www.nits.ac.in)

### About the course

This workshop focuses on progress in Silicon, Group III-V materials based emerging devices like tunnel FET, FINFET. This course will enhance knowledge and idea about design and optimization of devices and the integrated circuits that are indispensable for creation of new semiconductor chips. Modern chip design has become so complex that it is largely performed by specialized by software, which needs frequent up gradation to address advances in semiconductor technologies. In the recent international technology roadmap of semiconductor innovative materials, technologies and nano architectures have been explored. These emerging structures provide low power, high performance application. Further, these can be used as high frequency and memory applications. Using Computer Aided Technology tools, these emerging devices can be modeled. The main objective of this workshop are

- 1.To train and educate the device physics, AC, DC analysis, noise and temperature performance of the emerging device.
- 2.To explore the minds of researchers with the advancement VLSI area.
3. To provide hands on training on Technology Computer Aided Design tools.

### Tentative List of Resource Persons

1. Prof. S.C. Nandi- Indian Statistical Institute, Kolkata
2. Prof. Abhijit Mallik - University of Calcutta
3. Prof. Roy P. Paily- IIT Guwahati
4. Dr. Tanmoy Banerjee , Burdwan University
5. Dr. Saurabh Kumar Pandey, IIT Patna
6. Prof. S. Baishya, NIT Silchar

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#### Registration Form

Name (Block): \_\_\_\_\_  
\_\_\_\_\_

Gender: MALE / FEMALE

Designation: \_\_\_\_\_

Organization: \_\_\_\_\_

Mailing Address: \_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_

Telephone: \_\_\_\_\_

Email: \_\_\_\_\_

Educational Qualification: \_\_\_\_\_

Transaction ID: \_\_\_\_\_

Bank: \_\_\_\_\_

Date: \_\_\_\_\_

Required Accommodation (Paid): YES / NO

Signature of Applicant:

Date:

### CHIEF PATRON

Prof. Sivaji Bandyopadhyay, Director, NIT Silchar

### PATRON

Prof. F.A. Talukdar HoD, Dept. of ECE , NITS

### CHAIRMAN

Prof. S Baishya, Dept. of ECE , NIT Silchar

### CONVENER

Dr. Brinda Bhowmick

### ORGANIZING COMMITTEE MEMBERS

Dr. P K Paul, Mrs. M Paul, Dr. M Choudhury, Dr. K L Baishnab, Dr. W Arif, Dr. K Guha, Dr. T R Lenka, Dr. A Hossain, Sh. G P Kesri, Dr. A Nandi, Dr. B Basu, Dr. T khan, Dr. S K Tripathi, Dr. R.K.Karsh, Dr. C. Chowdhury, Dr. R Khosla, Dr. P Pattanayak, Dr. M Kavicharan, Dr. R Murugan, Dr P Puspa Devi, Dr. M V Swati, Dr. G S Baghel, and Dr. Tripti Goel,

### COORDINATOR

Dr. Ujjal Chakraborty  
Assistant Professor, Dept. of ECE  
Mobile No. +91 9563606800

