

NATIONAL INSTITUTE OF TECHNOLOGY SILCHAR

1ST SEM M. TECH ELECTRONICS & COMMUNICATION ENGG. IMPROVEMENT TABULATION SHEET, NOVEMBER 2019 (Provisional)

Microelectronics & VLSI Design.

Sl. No.	Registration No.	EC-501		EC-502		EC-503		EC 504		EC 526 (EI-I)		TCP	TGP/28	SPI/1st	CPI Below 6.0
		Semi-Con.Device Phy		Analog VLSI Circuits		DC Analysis of MOS Transistors		LAB -I		RF Design.					
		Credit	6	Credit	6	Credit	6	Credit	4	Credit	6				
1	18-24-108	CC	6	CD	5	F	0	AB	9	CC	6	28	138	4.93	***
2	18-24-109	CC	6	CD	5	F	0	BB	8	BC	7	28	140	5.00	***

Remarks: Improvement Subject marked as Red Colour.

BY ORDER